

AC 14/7/2016, Item No. 4.64

UNIVERSITY OF MUMBAI



Bachelor of Engineering

First Year Engineering (Semester I & II), Revised course
(REV- 2016) from Academic Year 2016 – 17,
(Common for All Branches of Engineering)

(As per **Choice Based Credit and Grading System**
with effect from the academic year 2016–2017)

From Coordinator's Desk:-

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) give freedom to affiliated Institutes to add few (PEO's) course objectives course outcomes to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, developed curriculum accordingly. In addition to outcome based education, **Choice Based Credit and Grading System** is also introduced to ensure quality of engineering education.

Choice Based Credit and Grading System enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes Faculty of Technology has devised a transparent credit assignment policy adopted ten points scale to grade learner's performance. Credit grading based system was implemented for First Year of Engineering from the academic year 2016-2017. Subsequently this system will be carried forward for Second Year Engineering in the academic year 2017-2018, for Third Year Final Year Engineering in the academic years 2018-2019, 2019-2020, respectively.

Dr. S. K. Ukarande
Co-ordinator,
Faculty of Technology,
Member - Academic Council
University of Mumbai, Mumbai

**Program Structure for
First Year Engineering (Semester I & II)
Mumbai University
(With Effect from 2016-2017)**

Semester I

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned					
		Theory	Pract.	Tut.	Theory	TW/Pract	Tut.	Total		
FEC101	Applied Mathematics-I	04	-	01	04	-	01	05		
FEC102	Applied Physics-I	03	01	-	03	0.5	-	3.5		
FEC103	Applied Chemistry -I	03	01	-	03	0.5	-	3.5		
FEC104	Engineering Mechanics	05	02	-	05	01	-	06		
FEC105	Basic Electrical Engineering	04	02	-	04	01	-	05		
FEC106	Environmental studies	02	-	-	02	-	-	02		
FEL101	Basic Workshop Practice-I	-	04	-	-	02	-	02		
Total		21	10	01	21	05	01	27		
Course Code	Course Name	Examination Scheme								
		Theory				End Sem Exam	Term Work	Pract	Oral	Total
		Internal Assessment								
		Test1	Test2	Avg						
FEC101	Applied Mathematics-I	20	20	20	80	25	-	-	125	
FEC102	Applied Physics-I	15	15	15	60	25	-	-	100	
FEC103	Applied Chemistry -I	15	15	15	60	25	-	-	100	
FEC104	Engineering Mechanics	20	20	20	80	25	-	25	150	
FEC105	Basic Electrical Engineering	20	20	20	80	25	-	25	150	
FEC106	Environmental studies	15	15	15	60	-	-	-	75	
FEL101	Basic Workshop Practice-I	-	-	-	-	50	-	-	50	
Total				105	420	175		50	750	

Semester II

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned				
		Theory	Pract.	Tut.	Theory	TW/Pract	Tut.	Total	
FEC201	Applied Mathematics-II	04	-	01	04	-	01	05	
FEC202	Applied Physics-II	03	01	-	03	0.5	-	3.5	
FEC203	Applied Chemistry -II	03	01	-	03	0.5	-	3.5	
FEC204	Engineering Drawing	03	04	-	03	02	-	05	
FEC205	Structured Programming Approach	04	02	-	04	01	-	05	
FEC206	Communication Skills	02	02	-	02	01	-	03	
FEL201	Basic Workshop Practice-II	-	04	-	-	02	-	02	
Total		19	14	01	19	07	01	27	
Course Code	Course Name	Examination Scheme							
		Theory				Term Work	Pract	Oral	Total
		Internal Assessment			End Sem Exam				
		Test1	Test2	Avg					
FEC201	Applied Mathematics-II	20	20	20	80	25	-	-	125
FEC202	Applied Physics-II	15	15	15	60	25	-	-	100
FEC203	Applied Chemistry -II	15	15	15	60	25	-	-	100
FEC204	Engineering Drawing	15	15	15	60	25	50	-	150
FEC205	Structured Programming Approach	20	20	20	80	25	25	-	150
FEC206	Communication Skills	10	10	10	40	25	-	-	75
FEL201	Basic Workshop Practice-II	-	-	-	-	50	-	-	50
Total				95	380	200	75	-	750

AC – 11.05.2017

Item No. 4.193

UNIVERSITY OF MUMBAI



Revised syllabus (Rev- 2016) from Academic Year 2016 -17

Under

FACULTY OF TECHNOLOGY

Computer Engineering

Second Year with Effect from **AY 2017-18**

Third Year with Effect from **AY 2018-19**

Final Year with Effect from **AY 2019-20**

As per **Choice Based Credit and Grading System**

with effect from the AY 2016–17

Co-ordinator, Faculty of Technology's Preamble:

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) and give freedom to affiliated Institutes to add few (PEO's). It is also resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges and experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, and developed curriculum accordingly. In addition to outcome based education, semester based credit and grading system is also introduced to ensure quality of engineering education.

Choice based Credit and Grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes and Faculty of Technology has devised a transparent credit assignment policy and adopted ten points scale to grade learner's performance. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 12-13 weeks and remaining 2-3 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

Choice based Credit and grading system is implemented from the academic year 2016-17 through optional courses at department and institute level. This will be effective for SE, TE and BE from academic year 2017-18, 2018-19 and 2019-20 respectively.

Dr. S. K. Ukarande
Co-ordinator,
Faculty of Technology,
Member - Academic Council
University of Mumbai, Mumbai

Chairman's Preamble:

Engineering education in India is expanding and is set to increase manifold. The major challenge in the current scenario is to ensure quality to the stakeholders along with expansion. To meet this challenge, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education and reflects the fact that in achieving recognition, the institution or program of study is committed and open to external review to meet certain minimum specified standards. The major emphasis of this accreditation process is to measure the outcomes of the program that is being accredited. Program outcomes are essentially a range of skills and knowledge that a student will have at the time of graduation from the program. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating the philosophy of outcome based education in the process of curriculum development.

As the Chairman, Board of Studies in Computer Engineering of the University of Mumbai, I am happy to state here that, the Program Educational Objectives for Undergraduate Program were finalized in a brainstorming session, which was attended by more than 85 members from different affiliated Institutes of the University. They are either Heads of Departments or their senior representatives from the Department of Computer Engineering. The Program Educational Objectives finalized for the undergraduate program in Computer Engineering are listed below;

1. To prepare the Learner with a sound foundation in the mathematical, scientific and engineering fundamentals.
2. To motivate the Learner in the art of self-learning and to use modern tools for solving real life problems.
3. To equip the Learner with broad education necessary to understand the impact of Computer Science and Engineering in a global and social context.
4. To encourage, motivate and prepare the Learner's for Lifelong- learning.
5. To inculcate professional and ethical attitude, good leadership qualities and commitment to social responsibilities in the Learner's thought process.

In addition to Program Educational Objectives, for each course of the program, objectives and expected outcomes from a learner's point of view are also included in the curriculum to support the philosophy of outcome based education. I strongly believe that even a small step taken in the right direction will definitely help in providing quality education to the major stakeholders.

Dr. Subhash K. Shinde
Chairman, Board of Studies in Computer Engineering,
University of Mumbai, Mumbai.

Program Structure B.E. Computer Engineering, (Rev. 2016) w.e.f. AY 2017-18

S. E. Computer Engineering (Semester-III)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract	Tut	Theory	TW/Pract	Tut	Total
CSC301	Applied Mathematics -III	4+1@	-	-	5	-	-	5
CSC302	Digital Logic Design and Analysis	4	-	-	4	-	-	4
CSC303	Discrete Mathematics	3+1@	-	-	4	-	-	4
CSC304	Electronic Circuits and Communication Fundamentals	4	-	-	4	-	-	4
CSC305	Data Structures	4	-	-	4	-	-	4
CSL301	Digital System Lab	-	2	-	-	1	-	1
CSL302	Basic Electronics Lab	-	2	-	-	1	-	1
CSL303	Data structure Lab	-	2	-	-	1	-	1
CSL304	OOPM(Java) Lab	-	2+2*	-	-	2	-	2
Total		21	10	-	21	5	-	26

@ 1 hour to be taken tutorial as class wise.

*2 hours shown as practical's to be taken class wise and other 2 hours to be taken as batch wise

Course Code	Course Name	Examination Scheme								
		Theory					TW	Oral	Oral & Pract	Total
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)				
		Test 1	Test 2	Avg.						
CSC301	Applied Mathematics -III	20	20	20	80	3	-	-	-	100
CSC302	Digital Logic Design and Analysis	20	20	20	80	3	-	-	-	100
CSC303	Discrete Structures	20	20	20	80	3	-	-	-	100
CSC304	Electronic Circuits and Communication Fundamentals	20	20	20	80	3	-	-	-	100
CSC305	Data Structures	20	20	20	80	3	--	-	-	100
CSL301	Digital System Lab	-	-	-	-	-	25	--	25	50
CSL302	Basic Electronics Lab	-	-	-	-	-	25	25	---	50
CSL303	Data structure Lab	-	-	-	-	-	25	-	25	50
CSL304	OOPM(Java) Lab	-	-	-	-	-	50	--	50	100
Total		100	100	100	400	-	125	25	100	750

Program Structure B.E. Computer Engineering, (Rev. 2016) w.e.f. AY 2017-18

S. E. Computer Engineering (Semester-IV)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract	Tut	Theory	TW/Pract	Tut	Total
CSC401	Applied Mathematics- IV	4+1@	-	-	5	-	-	5
CSC402	Analysis of Algorithms	4	-	-	4	-	-	4
CSC403	Computer Organization and Architecture	4	-	-	4	-	-	4
CSC404	Computer Graphics	4	-	-	4	-	-	4
CSC405	Operating System	4	-	-	4	-	-	4
CSL401	Analysis of Algorithms Lab	-	2	-	-	1	-	1
CSL402	Computer Graphics Lab	-	2	-	-	1	-	1
CSL403	Processor Architecture Lab	-	2	-	-	1	-	1
CSL404	Operating System Lab	-	2	-	-	1	-	1
CSL405	Open Source Tech Lab	-	2+2*	-	-	2	-	2
Total		21	12	-	21	6	-	27

@ 1 hour to be taken tutorial as class wise .

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Course Code	Course Name	Examination Scheme								
		Theory					TW	Oral	Oral & Pract	Total
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)				
		Test 1	Test 2	Avg.						
CSC401	Applied Mathematics- IV	20	20	20	80	3	-	-	-	100
CSC402	Analysis of Algorithms	20	20	20	80	3	-	-	-	100
CSC403	Computer Organization and Architecture	20	20	20	80	3	-	-	-	100
CSC404	Computer Graphics	20	20	20	80	3	-	-	-	100
CSC405	Operating System	20	20	20	80	3	--	-	-	100
CSL401	Analysis of Algorithms Lab	-	-	-	-	-	25	--	25	50
CSL402	Computer Graphics Lab	-	-	-	-	-	25	--	25	50
CSL403	Processor Architecture Lab	-	-	-	-	-	25	25	-	50
CSL404	Operating System Lab	-	-	-	-	-	25	-	25	50
CSL405	Open Source Tech Lab	-	-	-	-	-	25	---	25	50
Total		100	100	100	400	-	125	25	100	750

AC – 5th May, 2018

Item No. – 4.51

UNIVERSITY OF MUMBAI



Revised syllabus (Rev- 2016) from Academic Year 2016 -17

Under

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Computer Engineering

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**Chairman, Board of Studies in Computer Engineering,
University of Mumbai, Mumbai.**

Program Structure B.E. Computer Engineering, (Rev. 2016) w.e.f. AY 2018-19

T. E. Computer Engineering (Semester-V)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract	Tut	Theory	TW/Pract	Tut	Total
CSC501	Microprocessor	4	-	-	4	-	-	4
CSC502	Database Management System	4	-	-	4	-	-	4
CSC503	Computer Network	4	-	-	4	-	-	4
CSC504	Theory of Computer Science	3+1@	-	-	4	-	-	4
CSDLO 501X	Department Level Optional Course -I	4	-	-	4	-	-	4
CSL501	Microprocessor Lab	-	2	-	-	1	-	1
CSL502	Computer Network Lab	-	2	-	-	1	-	1
CSL503	Database & Info. System Lab	-	2	-	-	1	-	1
CSL504	Web Design Lab	-	2+2*	-	-	2	-	2
CSL505	Business Comm. & Ethics	-	2+2*	-	-	2	-	2
	Total	20	14	-	20	7	-	27

@ 1 hour to be taken tutorial as class wise.

*2 hours shown as Practical's to be taken class wise and other 2 hours to be taken as batch wise

Course Code	Course Name	Examination Scheme							Total
		Theory					TW	Oral & Pract	
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Avg.					
CSC501	Microprocessor	20	20	20	80	3	-	-	100
CSC502	Database Management System	20	20	20	80	3	-	-	100
CSC503	Computer Network	20	20	20	80	3	-	-	100
CSC504	Theory of Computer Science	20	20	20	80	3	-	-	100
CSDLO 501X	Department Level Optional Course -I	20	20	20	80	3	--	-	100
CSL501	Microprocessor Lab	-	-	-	-	-	25	25	50
CSL502	Computer Network Lab	-	-	-	-	-	25	25	50
CSL503	Database & Info. System Lab	-	-	-	-	-	25	25	50
CSL504	Web Design Lab	-	-	-	-	-	25	25	50
CSL505	Business Comm. & Ethics	-	-	-	-	-	50	-	50
	Total	100	100	100	400	-	150	100	750

Program Structure B.E. Computer Engineering, (Rev. 2016) w.e.f. AY 2018-19

T. E. Computer Engineering (Semester-VI)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract	Tut	Theory	TW/Pract	Tut	Total
CSC601	Software Engineering	4	-	-	4	-	-	4
CSC602	System Programming & Compiler Construction	4	-	-	4	-	-	4
CSC603	Data Warehousing & Mining	4	-	-	4	-	-	4
CSC604	Cryptography & System Security	4	-	-	4	-	-	4
CSDLO 601X	Department Level Optional Course -II	4	-	-	4	-	-	4
CSL601	Software Engineering Lab	-	2	-	-	1	-	1
CSL602	System software Lab	-	2	-	-	1	-	1
CSL603	Data Warehousing & Mining Lab	-	2	-	-	1	-	1
CSL604	System Security Lab	-	2	-	-	1	-	1
CSP605	Mini-Project	-	4	-	-	2	-	2
	Total	20	12	-	20	6	-	26

Course Code	Course Name	Examination Scheme								
		Theory					TW	Oral	Oral & Pract	Total
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)				
		Test 1	Test 2	Avg.						
CSC601	Software Engineering	20	20	20	80	3	-	-	-	100
CSC602	System Programming & Compiler Construction	20	20	20	80	3	-	-	-	100
CSC603	Data Warehousing & Mining	20	20	20	80	3	-	-	-	100
CSC604	Cryptography & System Security	20	20	20	80	3	-	-	-	100
CSDLO 601X	Department Level Optional Course -II	20	20	20	80	3	-	-	-	100
CSL601	Software Engineering Lab	-	-	-	-	-	25	25	--	50
CSL602	System Software Lab	-	-	-	-	-	25	--	25	50
CSL603	Data Warehousing & Mining Lab	-	-	-	-	-	25	--	25	50
CSL604	System Security Lab	-	-	-	-	-	25	---	25	50
CSP605	Mini-Project	-	-	-	-	-	25	---	25	50
	Total	100	100	100	400	-	125	25	100	750

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Co-ordinator,

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Chairman's Preamble:

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Dr. Subhash K. Shinde

**Chairman, Board of Studies in Computer Engineering,
University of Mumbai, Mumbai.**

Program Structure B.E. Computer Engineering, (Rev. 2016) w.e.f. AY 2019-20
B. E. Computer Engineering (Semester-VII)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract	Tut	Theory	TW/Pract	Tut	Total
CSC701	Digital Signal & Image Processing	4	-	-	4	-	-	4
CSC702	Mobile Communication & Computing	4	-	-	4	-	-	4
CSC703	Artificial Intelligence & Soft Computing	4	-	-	4	-	-	4
CSDLO 701X	Department Level Optional Course -III	4	-	-	4	-	-	4
ILO701X	Institute Level Optional Course-I	3	-	-	3	-	-	3
CSL701	Digital Signal & Image Processing Lab	-	2	-	-	1	-	1
CSL702	Mobile App. Development. Tech. Lab	-	2	-	-	1	-	1
CSL703	Artificial Intelligence & Soft Computing Lab	-	2	-	-	1	-	1
CSL704	Computational Lab-I	-	2	-	-	1	-	1
CSP705	Major Project-I	-	6	-	-	3	-	3
	Total	19	14	-	19	7	-	26

Course Code	Course Name	Examination Scheme								
		Theory					TW	Oral	Oral & Pract	Total
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)				
		Test 1	Test 2	Avg.						
CSC701	Digital Signal & Image Processing	20	20	20	80	3	-	--	-	100
CSC702	Mobile Communication & Computing	20	20	20	80	3	-	--	-	100
CSC703	Artificial Intelligence & Soft Computing	20	20	20	80	3	-	--	-	100
CSDLO 701X	Department Level Optional Course -III	20	20	20	80	3	-	--	-	100
ILO701X	Institute Level Optional Course-I	20	20	20	80	3	--	--	-	100
CSL701	Digital Signal & Image Processing Lab	-	-	-	-	-	25	--	--	25
CSL702	Mobile App. Development. Tech. Lab	-	-	-	-	-	25	--	25	50
CSL703	Artificial Intelligence & Soft Computing Lab	--	-	-	-	--	25	25	--	50
CSL704	Computational Lab-I						25	--	25	50
CSP705	Major Project-I	-	-	-	-	-	50	-	25	75
	Total	100	100	100	400		150	25	75	750

Program Structure B.E. Computer Engineering, (Rev. 2016) w.e.f. AY 2019-20

B. E. Computer Engineering (Semester-VIII)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract	Tut	Theory	TW/Pract	Tut	Total
CSC801	Human Machine Interaction	4	-	-	4	-	-	4
CSC802	Distributed Computing	4	-	-	4	-	-	4
CSDLO 801X	Department Level Optional Course -IV	4	-	-	4	-	-	4
ILO801X	Institute Level Optional Course-II	3	-	-	3	-	-	3
CSL801	Human Machine Interaction Lab	-	2	-	-	1	-	1
CSL802	Distributed Computing Lab	-	2	-	-	1	-	1
CSL803	Cloud Computing Lab	-	4	-	-	2	-	2
CSL804	Computational Lab-II	-	2	-	-	1	-	1
CSP805	Major Project-II	-	12	-	-	6	-	6
	Total	15	22	-	15	11	-	26

Course Code	Course Name	Examination Scheme								
		Theory					TW	Oral	Oral & Pract	Total
		Internal Assessment			End Sem. Exam	Exam Duration (in				
		Test 1	Test 2	Avg.						
CSC801	Human Machine Interaction	20	20	20	80	3	-	-	-	100
CSC802	Distributed Computing	20	20	20	80	3	-	-	-	100
CSDLO 801X	Department Level Optional Course -IV	20	20	20	80	3	-	-	-	100
ILO801X	Institute Level Optional Course-II	20	20	20	80	3	-	-	-	100
CSC801	Human Machine Interaction Lab						25	25	-	50
CSL802	Distributed Computing Lab	-	-	-	-	-	25	25	-	50
CSL803	Cloud Computing Lab	-	-	-	-	-	50	--	25	75
CSL804	Computational Lab-II	-	-	-	-	-	50	--	25	75
CSP805	Major Project-II						50	--	50	100
	Total	80	80	80	320	--	200	50	100	750

Sem.	Department Level Optional Course (DLOC)	Institute Level Optional Course (ILOC)
V	CSDLO5011: Multimedia System CSDLO5012: Advance Operating System CSDLO5013: Advance Algorithm	-----
VI	CSDLO6021: Machine Learning CSDLO6022: Advance Database System CSDLO6023: Enterprise Resource Planning CSDLO6024: Advance Computer Network	-----
VII	CSDLO7031: Advance System Security & Digital Forensics CSDLO7032: Big Data & Analytics CSDLO7033: Robotics	ILO7011. Product Lifecycle Management ILO7012. Reliability Engineering ILO7013. Management Information System ILO7014. Design of Experiments ILO7015. Operation Research ILO7016. Cyber Security and Laws ILO7017. Disaster Management & Mitigation Measures ILO7018. Energy Audit and Management ILO7019. Development Engineering
VIII	DLO8011: High Performance Computing DLO8012: Natural Language Processing DLO8013: Adhoc Wireless Network	ILO8021. Project Management ILO8022. Finance Management ILO8023. Entrepreneurship Development and Management ILO8024. Human Resource Management ILO8025. Professional Ethics and CSR ILO8026. Research Methodology ILO8027. IPR and Patenting ILO8028. Digital Business Management ILO8029. Environmental Management