

Agnel Charities

Fr. C. Rodrigues Institute of Technology, Vashi, Navi Mumbai, Maharashtra

Journal Publications

1. **M. K. Namboothiripad**, M. J. Datar, M. C. Chandorkar and S. B. Patkar, "FPGA Accelerator for Real-Time Emulation of Power Electronic Systems Using Multiport Decomposition," in IEEE Transactions on Industry Applications, vol. 56, no. 6, pp. 6674-6686, Nov.-Dec. 2020, doi: 10.1109/TIA.2020.3024347.
2. George, S., **Mini, K. N.**, and Supriya, K., "Optimized Reactive Power Compensation Using Fuzzy Logic Controller", Journal of The Institution of Engineers (India): Series B, vol. 96, no. 1, pp. 83–89, 2015. doi:10.1007/s40031-014-0127-7.
3. Arpita Das, **Mini K Namboothiripad**, "Voltage Control of Buck Converter using Sliding Mode Controller", International Journal of Engineering Research & Technology, ISSN: 2278-0181, Vol. 3 Issue 4, April – 2014.
4. **M. K. Namboothiripad**, "Genetic Algorithm Based SVC Switching for Harmonic and Reactive Power Compensation", International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control engineering, Volume 1, Issue 7, October 2013.
5. Bindu R, **Mini N.**, "Tuning of PID Controller for DC Servo Motor using Genetic Algorithm", International Journal of Emerging Technology and Advanced Engineering, ISSN 2250-2459, Volume 2, Issue 3, March 2012.

Conference Publications

1. **M. K. Namboothiripad**, M. J. Datar, M. C. Chandorkar and S. B. Patkar, "Accelerator for Real-Time Emulation of Modular-Multilevel-Converter Using FPGA," 2020 IEEE 21st Workshop on Control and Modeling for Power Electronics (COMPEL), Aalborg, Denmark, 2020, pp. 1-7, doi: 10.1109/COMPEL49091.2020.9265684.
2. Y. Mahajan, S. Obla, **M. K. Namboothiripad**, M. J. Datar, N. N. Sharma and S. B. Patkar, "FPGA-Based Acceleration of LU decomposition for Analog and RF Circuit Simulation," 2020 33rd International Conference on VLSI Design and 2020 19th International Conference on Embedded Systems (VLSID), Bangalore, India, 2020, pp. 131-136, doi: 10.1109/VLSID49098.2020.00040
3. **M. K. Namboothiripad**, M. J. Datar, M. C. Chandorkar and S. B. Patkar, "FPGA Accelerator for Real-Time Emulation of Power Electronic Systems Using Multiport Decomposition," 2019 National Power Electronics Conference (NPEC), Tiruchirappalli, India, 2019, pp. 1-6, doi: 10.1109/NPEC47332.2019.9034784.
4. Maitreyee D, **MK Namboothiripad**, "DSP Based Second Order Sliding Mode Controller for Buck Converter", in 3rd IEEE International Conference on Convergence in Technology 2018, Pune, India, held on 6-8 April 2018.
5. M. Deshmukh, **MK Namboothiripad** "A constant frequency second order sliding mode controller for buck converter," 2017 Second International Conference on

Electrical, Computer and Communication Technologies (ICECCT), Coimbatore, 2017, pp. 1-5, doi: 10.1109/ICECCT.2017.8118001.

6. M Mane, **MK Namboothiripad**, "PWM based sliding mode controller for shunt active power filter", International Conference on Nascent Technologies in Engineering (ICNTE), pp. 1-6, February 2017.
7. M Mane, **MK Namboothiripad**, "Current harmonics Reduction using sliding Mode Control Based Shunt Active Power Filter", 10th International Conference on Intelligent Systems and Control (ISCO), 2016 Coimbatore vol.01, pp.93-98.
8. Kunal Pardeshi, **MK Namboothiripad**, "Robust Sliding Mode Controller for PV Connected Converter Inverter System", 2nd international conference on Emerging trends in Technology and Applied sciences at SAINTGITS COE Kottayam in 2015, Kerala.
9. A Das, **MK Namboothiripad**, "Sliding Mode Controller with Adaptive Sliding Coefficient for Buck Converter", 6th IEEE Power India International Conference, Power Electronics; PIICON -2014.
10. S Bindu, **N Mini**, "Reactive power compensation in three phase circuits by different power definitions", TENCON-2008, IEEE Hyderabad section.



