

BIO-DATA



1.	Name		Divya Sajeesh			
2.	Designation		Asst.Professor			
3.	Residential Address		A/5, Meenakshi CHS, Sector 16, Plot 38, New Panvel			
4.	Date of birth		28/02/1982			
5.	Total Experience					
	i.	Teaching	12 Yrs			
	ii.	Industrial	NA			
6.	Qualifications					
	Exam Passed	Year	Institution/ University	Branch/Specialization	Percentage/CGPI	
	B.Tech	2005	Mahatma Gandhi	Electrical & Electronics	73%	
	ME	2014	Mumbai	Power Electronics & Drives	8.5 CGPI	
Additional Qualification: NA						
7.	Employment Record					
	Institution		Year (From To)		Designation	
	Fr.C.Rodrigues Institute of Technology		Aug 2009 till date		Asst.Professor	
	V.P.M's Polytechnic, Thane		July 2007 to July2009		Lecturer	
8.	Undergraduate / Postgraduate Teaching Experience and Subjects Taught					
	Subjects Taught at UG level					
	Sr.No.	Name of Subject			Semester	
	1	Electrical Networks			III	
	2	Conventional and Non conventional Power Generation			III	
	3	Electronic Devices and Circuits			III	
	4	Electrical Power System			IV	
	5	Analog and Digital Integrated circuits			IV	
	6	Power Electronics			V	
	7	Power System Analysis			VI	
	Subjects Taught at PG level					
	Sr.No.	Name of Subject			Semester	
		HVDC			I	
9.	Research Experience					
10.	Research Funding / Consultancy Services:					
	Sr.No.	Name of the Company	Address	Product	Consulting Service	Consulting Fees
	Research Grants:					
	Sr.No.	Name of Funding Organization	Type of Grant	Amount (Rs.)	Year	Name of Research Project
		IEI	PG Grant	80000	2012	Hardware

					Implementation of PWM Rectifier
Technical Collaboration / Lab Funding with Industries					
	Sr.No.	Name of the Funding Organization	Type of Support	Amount (Rs.)	Year
		NA	NA	NA	NA
11.	Professional Societies Fellowship / Membership				
	ISTE(2016)		LM112288		
	IEI		AM150778-0		
12.	Achievements / Awards / Position: Best paper Award for the paper titled “Modulation Techniques in Single Phase PWM Rectifier” in International conference on Advances in science and Technology, Mumbai, 8-9 Dec 2015				
13.	Projects guided in UG/PG level:				
14.	Short Term Training Programmes attended				
	<ul style="list-style-type: none"> • 5 days ATAL Academy online elementary FDP on “Power Electronics for Power Systems” from 6th Dec 2021 to 10th Dec 2021 at RIT, Kottayam, Kerala • 5 days ATAL Academy online elementary FDP on “Power Electronic Systems and its Real Time Control Implementation in DSP” from 8th Nov 2021 to 12th Nov 2021 at FCRIT, Vashi, Maharashtra • One week ISTE approved online STTP on “DSP, Arduino, and C/Python”, 10th May to 14th May 2021, FCRIT, Vashi • One week AICTE-ISTE approved Induction/Refresher Programme on “Teaching Pedagogies for Engineering Education”, 24th to 28th June 2019, FCRIT, Vashi • One week AICTE-ISTE Induction/Refresher Programme on “Innovative Teaching Learning Practices to Achieve Outcome based Education and Accreditation”, 3rd to 9th May 2018 at SPIT, Andheri. • One week AICTE-ISTE approved STTP on “Internet of Things A Practical Approach” Nov 27th – Dec 2nd 2017 at ACPCE Kharghar. • Two week STTP on “Electric Power System” at BVCE, Kharghar, conducted by IIT Kharagpur 2017 • Five days STTP on “Microgrid, Smartgrid and Futuristic Energy Paradigms” at FCRIT, Vashi 2016 • Two day FDP on “Teaching Power Electronics with MATLAB/Simulink” at RAIT, Nerul, Navi Mumbai, 2015. • Two day workshop on “Simulation of Power Electronic Circuits” at IIT Bombay, 2014. • Five day STTP on “Workshop on Advanced Control System” at FCRIT, Vashi, 2013. • Six days FDP on “Communication Skills” at FCRIT, Vashi, 2013. • Five day STTP on Renewable Energy Technologies & Applications” at FCRIT, Vashi 2011. • Five days STTP on “Role of Technology in Environmental Conservation” at SIES Nerul 2010 				
15.	<ul style="list-style-type: none"> • List of Journal Papers Published: Shruti Gite, Hage anga, Sheetal Bhawe, Divya Sajeesh “Power Factor and Harmonic Analysis in Single Phase AC to DC Converter” International Journal of Engineering Research and Technology, 2015, ISSN 2278- 0181 				
16.	List of Papers Published in National and International Conferences:				
	<ul style="list-style-type: none"> • Divya Sajeesh, Sincy George, “Hardware Implementation of PWM Rectifier” 				

	<p>ICASTM, Dec 23rd -24th 2021</p> <ul style="list-style-type: none"> • Mini Rajeev, Divya Sajeesh, “Harmonic Compensation by transformer less Grid tied PV inverter using Conservative Power Theory”, 5th International Conference for Convergence in Technology , 29th -31st March, 2019 • Poornima Rao, Divya Sajeesh, “Power Generation using Solar Roof Top Sytem: A Case study”, International Conference on Advancement in Engineering Applied Science and Management, Mumbai, 10 October 2017 • Felix Keettikkal, Divya Sajeesh, Poornima Rao, Shashank Hande, Ganesh Dakave Tushar Kute, Akshay Mahajan, R.D.Kulkarni “Generation of Heat on Fuel rod by using Induction Heating”, International Conference on Nascent Technologies in Engineering field, Mumbai, 9-10 January 2017, DOI: 10.1109/ICNTE.2017.7947943 • Ramkumar Maurya, Divya Sajeesh, Poornima Rao, “Utility System Unbalance in Single phase electric Traction-A Review” International conference on Global Technology Initiatives, Mumbai, 29-30 March 2016 • Arpit Bohra, Divya Sajeesh & Michael.S, “Modulation Techniques in Single Phase PWM Rectifier”, International conference on Advances in science and Technology, Mumbai, 8-9 Dec 2015 • Divya Sajeesh, Sincy George, “Power Factor Improvement in Rectifier Circuit –A Simulation study”, IEEE international conference on Magnetics, Machines & Drives, Kerala, 24-26 July 2014, DOI: 10.1109/AICERA.2014.6908237
17.	<p>Invited Lectures in FDP/ STTP: Conducted a lab session on “Simulation tutorials on PWM rectifiers” for STTP on Software Tools for Engineering Researchers’ organized by electrical Department of FCRIT, Vashi</p>