

BIO-DATA



Recent Photograph

1.	Name	Abhishek Kanchan Shiwalkar					
2.	Designation	Assistant Professor					
3.	Residential Address	A-502 Neelkanth Sadan, Sector 10, Khanda Colony, Navi Mumbai, 410206					
4.	Date of birth	18/10/1988					
5.	Total Experience	12					
i.	Teaching	12					
ii.	Industrial	0					
6.	Qualifications						
	Exam Passed	Year	Institution/ University	Branch/Specialization	Percentage/CGPI		
	M.E.	2016	Pune University	Electrical Power System	7.44		
Additional Qualification:							
7.	Employment Record						
	Institution	Year (From To)		Designation			
	RSCOE, Pedhambe, Chiplun	20-3-2012 to 25-7-2016		Assistant Professor			
	FCRIT, Vashi	27-6-2017 till date		Assistant Professor			
8.	Undergraduate / Postgraduate Teaching Experience and Subjects Taught						
	Subjects Taught at UG level						
	Sr.No.	Name of Subject				Semester	
	1	Drives and Control				VIII	
	2	Microprocessor and Microcontroller				VI	
	3	Power System Analysis				VI	
	4	Electrical Machines-I				III	
	5	Electrical Machines-II				IV	
	6	Electrical Machines-III				V	
	7	Electrical Machines-IV				VI	
	8	Conventional and Nonconventional Power Generation				III	
	9	Basic Electronics				III	
	10	Electrical Machines-III (Rev.2016)				VI	
	11	Fundamental of Electrical Machines and Measurement				III	
	12	Basic Electrical Engineering				I	
	13	Electrical AC Machines-II				V	
	Subjects Taught at PG level						
	Sr.No.	Name of Subject			Semester		
	1	Extra High Voltage AC transmission system.			II		
9.	Research Experience						
10.	Research Funding / Consultancy Services:						
	Sr.No.	Name of the Company	Address	Product	Consulting Service	Consulting Fees	Period

Research Grants:						
Sr.No.	Name of Funding Organization	Type of Grant	Amount (Rs.)	Year	Name of Research Project	
1	FCRIT	R&D Activity	49100	2021-22	To design and develop Solar Water Pumping System	
Technical Collaboration / Lab Funding with Industries						
Sr.No.	Name of the Funding Organization	Type of Support	Amount (Rs.)	Year		
11.	Professional Societies Fellowship / Membership ISTE membership IEI membership					
12.	Achievements / Awards / Position					
13.	Projects guided in UG/PG level 1-Line voltage compensation using Dynamic Voltage Restorer. 2- Sign language recognition.					
14.	<p>Short Term Training Programmes attended</p> <ol style="list-style-type: none"> 1. Three day course on “Solar Photovoltaic Technologies : Introduction and Current status”, conducted by IIT Bombay ,OCT. 2010 2. Application of Microcontroller in Electrical Engineering”, conducted by Department of electrical engineering , BSCOER ,Narhe ,Pune, October 2013. 3. ‘Reliability in Electrical and Electronic Systems ’organized by the department of electrical engineering at FCRIT ,Vashi ,July 2017 4. NPTEL course on ‘Power System Engineering’,6 month duration 5. Electric Power Systems an online non-credit course authorized by University at Buffalo and The State University of New York and offered through Coursera 6. Three days online workshop on ‘Education 4.0’, organized by Atharva College Of Engineering, Malad, Mumbai. 7. One-week Online Faculty Development Program on "Opportunities & Challenges in Electronics & Allied Industries in India post COVID-19" organized by Vivekanand Education Society's Institute of Technology, Chembur, Mumbai. 8. One-week online Faculty Development Program on “OUTCOME BASED EDUCATION: A STEP TOWARDSEXCELLENCE” from 11-15 May 2020 under Margdarshan Scheme of AICTE, New Delhi. 9. Completed a certificate course on “ Learn to design your own solar Home System”, organized by Energy Swaraj Foundation. 10. One week online STTP on topic “DSP, Arduino, and C/Python” organized by Department of Electrical Engineering of FCRIT, Vashi from 10th May 2021 to 14th May 2021. 11. 5 days online Faculty Development Program on the topic “Futuristic Electrical Power Systems” organized by Department of Electrical Engineering of FCRIT, Vashi from 10th-16th November 2022. 12. AICTE Training And Learning (ATAL) 13. Academy Faculty Development Program on Institutional Restructuring and Strengthening Autonomy through NEP at FR. CONCEICAO RODRIGUES COLLEGE OF ENGINEERING from 20/11/2023 to 25/11/2023. 14. ISTE approved SF-STTP/ FDP on “Pedagogical Strategies for Effective Teaching-Learning” held during 18.12.2023 to 23.12.2023 organized by Fr. C. Rodrigues Institute of Technology, Vashi, Navi Mumbai, Maharashtra. 					
15.	List of Journal Papers Published (list in IEEE format)					
16.	List of Papers Published in National and International Conferences (list in IEEE format)					

	<p>1-Abhishek Shiwalkar and R P Kelapure , " Application Of Cascade Cockcroft Walton Voltage Multiplier In Multiple Output DC-DC Converter," International Journal of Innovation In Engineering and Science,2016.</p> <p>2- Abhishek Shiwalkar, Bennadit Nadar, Steven Alappat, Ashley Noronha, Nathaniel Xavier," Design of power drive for electric vehicle with solar charging", International Journal of Research in Engineering and Innovation, Volume-3, Issue-1, pp- 72-78, 2019</p> <p>3-Abhishek Shiwalkar and Shilpa Shinde, "Multiple output dc-dc converter derived from Cock-Croft Walton voltage multiplier and SIMO converter", International Journal of Research in Engineering and Innovation Vol-4, Issue-3,pp-137-142, 2020.</p> <p>4- Shreya Shinde, Lennard Dsouza, Snehal Maria, Shivam Anvekar, Abhishek Shiwalkar, "Data- Driven Modelling of Prognostics of Lithium-ion Batteries using LSTM" International Research Journal of Engineering and Technology, Volume-9, Issue-6, June 2022.</p> <p>5- S. Varma and A. Shiwalkar, "Non Contact Voltage with Electrostatic Polarity Detector," <i>2023 World Conference on Communication & Computing (WCONF)</i>, RAIPUR, India, 2023, pp. 1-5, doi: 10.1109/WCONF58270.2023.10235014.</p> <p>6- Rane, Mahendra, Rashmi G. Kale and Abhishek Shiwalkar. "Stability Analysis of Quadratic Boost Converter with Different Controllers." <i>2023 5th Biennial International Conference on Nascent Technologies in Engineering (ICNTE) (2023)</i>: 1-6.</p>
17.	Books/Reports/General articles etc.
18.	Invited Lectures in FDP/ STTP
19.	International Conference Technical Program Committee Member / Reviewer
20.	Patents