Department of Computer Engineering					
Name	Dr. Lata Ragha				
Designation	Professor in Computer Engineering, Dean, Student Affairs and Alumni				
Date of Joining the Institute	26-12-2016				
Education Qualifications	UG	PG		Phd	
	BE (CSE)	M. Tech (CSE)		PhD (Dept. of CSE)	
Work Experience in years	36 years (Teaching)				
Area of Specialization	Networks and Security, Routing Protocols, Data Mining, AIML				
Courses taught	Under Graduate Level		Post Graduate Level		
	 Data Structure Computer Graphics Database Management System Computer Networks System Programming and Compiler Construction Computer Simulation and Modelling Cryptography and System Security Adhoc Wireless Networks Parallel and Distributed Computing Distributed Computing Advanced Security and Digital Forensics 		 Advanced Computing and Network Design Parallel Computing Object Oriented Analysis and Design Advanced Database Systems Research Methodology 		
Research / Projects guidance (Number of Students)	Under Graduate projects		More than 80		
	Masters		39 (2005 to 2016)		
	Ph.D (Completed / Thesis Submitted / Ongoing)		7 + 2 + 3		
External Fund Received	 Agent Based reconfigurable routing for MPLS Network, RPS, 2007-08 National workshop on Emerging trends in Pattern Recognition, BRNS, September 2010. Development and Modernization of Advanced Network Laboratory for Network Design, Monitoring and testing, MODROB, 2013-14. M-Vision, Minor research grant, MU, 2018-19. 				

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Patent (Filed /	1. A system to manage trust and to enhance the security of a Mobile Ad- Hoc Network, 201821034696, 10/09/2018, page no.37567			
published / Granted) details				
details	2. A system to manage emergency situations on road with the utilization			
	of vehicular communication, 201821045249, 2018.			
	3. A method of implementing a Vigorous and Effective Trust Model for			
	Security Enhancement in MANET, 202121033330, 24/07/2021			
	4. A system to improve the accuracy of sentiment analysis to detect			
	whether the user is depressed, 202241013498 dated 11/03/2022			
	5. An intelligent approach to monitoring the efficiency of protected block			
	chain-based healthcare facility, 202221056871 dated 04/10/2022			
	6. Smart Bug Detector, Design No. 407793-001, Issue No. 17/2024			
	dated 26/04/2024.			
Research Publications (No. of papers in National/International Journals / Conferences and No. of Books/	Scopus/SCI indexed journals: 18			
	Peer reviewed / UGC journals: 52			
	IEEE/Springer conferences: 40			
	Books/Book Chapters: 05			
	Other International Conferences: 28			
Book Chapters published	National Conference: 14			
Other major	Member, BoS, Information Technology, University of Mumbai (2 times:			
responsibilities	from 2018-19 and from 2023-24)			
	 Member, VC nominee, BoS, Information Technology, K. J. Somaiya 			
	Institute of Technology, Mumbai (2 times: from 2018-19 and from			
	2024-25)			
	Member, VC nominee, BoS, Cyber Security, SAKEC, Chembur (2023-			
	24 to 2025-26)			
	Member, Department advisory board at SIESGST, Nerul; VPPCOE,			
	Sion; SAKEC, Chembur; Xavier Institute of Engineering, Mahim;			
	SIGCE, Koparkherne.			
	University nominee and subject expert for conducting USSC interviews			
	Convener/Member for LIC visit, University of Mumbai			
	Reviewer/Chairperson for various International Conferences			
	Reviewer/Examiner for PhD Thesis outside home university			
	Chairperson for PhD Viva-voce Exam, University of Mumbai			
	Resource person for STTPs			
	Department Academic Audit conducted at KJSIT, FCRCE, XIE, SIESGST.			