

**Name:** Shalaka S. Deshpande.

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**Carrier objective:**

Technical engineer position in a fast paced engineering lab related to current and new technology. To learn new aspects of technology and modify current trends in embedded systems .To work on projects leading for socio economic growth of institute and society and bridge gap between academics and industry.

**Qualification:**

- 1) M.E in Embedded Systems from B.A.M.U university ,MIT Aurangabad with 62% marks in 2017
- 2) B.E. in Electronics and Communication from B.A.M.U university ,MIT Aurangabad with 62% marks in 2012
- 3) HSC from Maharashtra board Aurangabad with 74% marks in 2008
- 4) SSC from Maharashtra board Aurangabad with 83% marks in 2006

**Professional skills:**

Operating Systems	Windows
Programming Languages	Assembly, VHDL, C
Embedded Software	MATLAB, VIVADO ,Keil, Proteus
Other Software	Xilinx, Multisim

**Experience:**

- 1)Worked as an Teaching Assistant in Maharashtra Institute of Technology Aurangabad for 1 year 4 monthes.
- 2)Worked as an Assistant Professor in SIES graduate school of technology , Nerul, Navi Mumbai for 11 monthes.

3) Worked as a Teacher in Aggarwal classes from 26<sup>th</sup> July 2021 to 28<sup>th</sup> October 2021.

4) Worked as an Assistant Professor in Chembur Karnataka Night College.

5) Worked as an embedded trainer and developer in embedded technologies from 10 March 2022 to 2<sup>nd</sup> May 2022.

6) Worked as an Assistant Professor in ARMIET College from 13/02/2023 to 30/09/2023. Currently working in Father Agnells Vashi, Navi Mumbai.

7) Took FDP on Successful management and Introduction to Corporate world in ARMIET College on 8/06/2024

**Subjects Taught:**

1) Digital Image Processing

2) Basic Electrical Engineering

3) Digital signal processing

4) Computer Organization And Architecture.

5) Microprocessor.

6) Computer Networks.

7) Entrepreneurship and E business.

8) Artificial Intelligence and Data Science.

**Projects:**

1) Tomographic Image Reconstruction using Artificial neural network.

2) FPGA implementation of Viterbi Algorithm

**Professional Courses undertaken:**

1) Completed short term training program on Neural network and Tensor flow from 2<sup>th</sup> January to 7<sup>th</sup> January 2019 in SIESGST

2) Completed short term training program on "Transforming higher education Mileu towards excellence" from 30<sup>th</sup> April to 10<sup>th</sup> May in SIESGST

3) Completed one day workshop on "Machine Learning " held at SIESGST on 23<sup>rd</sup> March 2019.

4)Completed one day workshop on “FPGA” held at SIESGST on 16<sup>th</sup> March 2019

5)Completed workshop on “MATLAB and its application in communication and Neural Networks” organized by MIT Aurangabad from 19<sup>th</sup> to 20<sup>th</sup> February 2011

6)Advanced Embedded systems using Linux from 10<sup>th</sup> June 2019 to 10<sup>th</sup> September 2019.

7)Cyber Security Bootcamp from 17<sup>th</sup> January 2024 to 19<sup>th</sup> January 2024.

**Publications:**

Sr no	Title	Journal
1	FPGA implementation of Viterbi Algorithm in grand alliance ATSC system	International journal of electrical, electronics and data communication.
2	Advanced Television System Committee and standard modulation schemes	International journal of advances in science engineering and technology.

**Area of interest:** Embedded Systems and VLSI

**Hobbies:** Reading books

**Key strengths:**

- 1)Good Analytical Skills.
- 2)Self-confident.
- 3)Better time management capability.
- 4)Work efficiently in group as well as individually.

**Personal profile:**

Name: Shalaka S Deshpande

Date of birth: 23<sup>rd</sup> July 1990

Sex: Female

Language Known: English, Hindi ,Marathi

Has Lecture videos on youtube channel

**Name of youtube channel:-Shalaka Deshpande**

**Shalaka Deshpande Youtube channel has 728 subscribers and more than 66 lectures.**

### **About the Youtube Channel:-**

I have covered the basic concepts of Embedded systems .Topics related with core , memory , Introduction ,applications and communication interface(Internal and external) are covered. Have also covered concepts of computer networks and digital image processing on youtube channel.I am also planning to cover programming in C and Embedded C and detailed architecture of Artix-7 board which I have used for Project. I have the hobby of reading technical books and I am very volatile. I feel research attitude is important in technical field all my videos are prepared after putting research in that concept

### **Reference Books:-**

- 1)Introduction to Embedded Systems by Shibu KV.
- 2)Embedded systems, Architecture, Programming and design by Rajkamal.
- 3)Computer Networks by Forouzan.
- 4)Digital Image Processing by Shridhar(Oxford press).