

Profile & Objective:

- Ph.D. pursuing in Electronics & Telecommunication Engineering (Course work completed)
- First Class- M.E.(Electronics & Telecommunication Engineering).
- Distinction – B.Tech. (Electronics & Telecommunication Engineering).
- Dedicated and hardworking individual exploring challenging teaching and coaching assignments in a professional educational institute.

Strengths:

- Excellent relationship management and strong communication skills.
- Hard work & Ability to survive under any environment.

Teaching Experience: Total Experience – 14 Years

- As a Assis. Prof. in Fr.C.R.I.Technology, Vashi in Electronics & Telecommunication Engineering Department from January 2021 to Nov 2021.
- As a Assis. Prof. in Fr.C.R.I.Technology, Vashi in Electronics & Telecommunication Engineering Department from August 2018 to April 2019.
- As a Assis. Prof. in MGM College of Engineering and Technology, Kamothe in Electronics & Telecommunication Engineering Department from 1st July 2014 to 30th June 2018.
- As a Lecturer and HoD in SES GOIFE, Karjat in Electronics & Telecommunication Engineering Department from 11th July 2013 to 30th June 2014.
- As a Lecturer in SIES, Nerul in Electronics & Telecommunication Engineering Department from 26th June 2012 to 24th Nov 2012.
- As a Lecturer in Don Bosco Institute of Engineering, Kurla in Electronics & Telecommunication Engineering Department from 7th July 2008 to 18th July 2011.
- As a Lecturer in K.J. Somaiya College of Engineering, Vidyavihar in Electronics & Telecommunication Department from 26th July 2006 to 1st July 2008.
- As a Lecturer in Shah & Anchor Kuttchi Polytechnic, Chembur in Computer

Department from 26th July 2005 to 25th July 2006.

- As a Technical Member in **Jetking School of Electronics Technology**, Chiplun for Hardware & Networking from 17th July 2000 to 31st Aug.2001.

Achievements:

- Working as a **NAAC Committee member**, @MGM CET 2017-18, @FCRIT 2019, FCRIT 2021, Navi Mumbai
- Worked as a Event Convener of Technical Festival “INFINITY2k14” on 13th to 15th MAR’2014 at SES GOIFE, Karjat.
- Arranged Industrial Visit to GMRT, Narayangaon, Pune of SES GOIFE students.
- Deliver a lecture in a Workshop on ‘**PC Assembling**’ for Faculty Members of Dept. of EXTC, Don Bosco Institute of Technology, Kurla.
- Worked as a Event Coordinator of Circuit Design Contest at Don Bosco Institute of Technology, Kurla.
- Worked as a Event Coordinator of ‘Robotryst’10 on 6th &7th MAR’2010 at Don Bosco Institute of Technology, Kurla.
- Independently held interclass project competition for practical knowledge development of Second Year students in the subject ‘Logic Design’.
- Was part of team implementing ISO 9001 in the Shah & Anchor Kuttchi Polytechnic, Chembur.

Academic Qualification:

- **Master of Engineering** (Electronics & Telecommunication Engineering) Graduated from Sardar Patel Institute of Technology, Mumbai in the year 2013 with **65.22% (First Class)** marks.
- **Bachelor of Engineering (Electronics & Telecommunication)** Graduated from D.B.A.T.U., Lonere, Raigad in the year 2000 with **66.22% (Distinction)** marks.

Additional Information :

- Software Course completed in **Python for Data Science, C, C++, Visual Basic, PC Assembling and Networking.**
- **Subjects taught in EXTC Engineering** - Computer Communication and Telecom Networks, Wireless Networks , Digital Electronics, Analog Communication, RF modeling and Antennas, Mobile Communication, Digital VLSI Design
- **Subjects taught in Computer Engineering** – C, Python, Computer Network, Mobile Communication and Computing

STTP Attended : 10

Paper Publications: 13

- i) International Conferences : 05 (IEEE - 02), (2 SCOPUS Indexed with citations 41,34)
- ii) International Journal : 06 (Free Journals -02)
- iii) National Conferences : 02

Personal Details:

Name: Akshata A. Raut.
Date of Birth: 15th Aug 1978
Gender: Female
Marital Status: Married
Languages known: English, Hindi, Marathi.
Permanent Address: A5-901, Plot no, 8,9,10, Sector-35 G, Hyde Park,
Kharghar, Navi Mumbai-410210

Declaration: I hereby declare that all the above information is true and to the best of my Knowledge.

(Akshata A. Raut)

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Google Scholar: <https://scholar.google.com/citations?user=tRepGpgAAAAJ&hl=en>

LinkedIn: www.linkedin.com/in/akshata-raut-463a9b125

Attached List of research paper publications.

Paper Publications

International Journal : 06 (Free Journals -02)

- 1] Panda, Siba & Achyut, Konasagar & Kulkarni, Swati & Raut, Akshata & Nayak, Aayush. (2023). An Organized Literature Review on Various Cubic Root Algorithmic Practices for Developing Efficient VLSI Computing System—Understanding Complexity. 10.1002/9781119857891.ch3, by **Wiley-Scrivener Publisher**.
- 2] Achyut, Konasagar & Kulkarni, Swati & Raut, Akshata & Panda, Siba & Nair, Lakshmi. (2022). An intelligent verification management approach for efficient VLSI computing system. 10.1049/PBPC054E_ch3, by **IET publisher**.
- 3] H. Shekhar, A. Jha, M. Gaikwad, A. Raut, “Over Speed Surveillance using IoT”, Published in *International Journal for Research in Applied Science and Engineering Technology (IJRASE)*,(ISSN : 2321-9653) for issue no. 4 of volume 6, April 2018, pp. 2872-2874.
- 4] P. Revane, S. Salaskar, K. Shelke, P. Tawar, A. Raut, “Implementing an IoT based Antenna Positioning System”, Published in *International Journal for Research in Applied Science and Engineering Technology (IJRASE)*,(ISSN : 2321-9653) for issue no. 4 of volume 6, April 2018, pp. 2898-2900.
- 5] S. Kulkarni, A. Raut, V. Walekar, “A Privacy Preserving Scheme for Data Security in the Video Surveillance” published in *IOSR Journal of Electronics and Communication Engineering (IOSR-JECE)* e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 12, Issue 3, Ver. II, May - June 2017, PP. 23-30.
- 6] A. Raut, N. Bhagat, S. S. Rathod, "Comparative Analysis of Transimpedance Amplifier of 45 nm and 180 nm CMOS Technology," published in *International Journal of Scientific Engineering and Technology* (ISSN : 2277-1581)" for issue no. 10 of volume 2,Oct.2013.

• International conferences :05 (IEEE - 02), (2 SCOPUS Indexed with citations 41,34)

- 1] S. Hunnar, A. Raut, S. Kulkarni, “Implementation of image processing for detection of brain tumors”, published in *IEEE Computer Society International Conference on Intelligent Computing and Control Systems (ICICCS)*, June 2017. [[Scopus Indexed, Citation – 41](#)]
- 2] S. S. Hunnur, A. Raut and S. Kulkarni, "Implementation of image processing for detection of brain tumors," 2017 International Conference on Computing Methodologies and Communication (ICCMC), 2017, pp. 717-722, doi:10.1109/ICCMC.2017.8282559. [[Scopus Indexed, Citation – 34](#)]
- 3] A. Raut, S. Borsune, S. Shapure, "Comparative Analysis of Transimpedance Amplifier of 32nm, 45 nm and 180 nm CMOS Technology," published in *ICETTA-2014*,ISBN-978-81-923777-7-3, 7 and 8th march 2014.
- 4] S. Shapure, S. Borsune, A. Raut, "More than Thirty years Attacks on RSA- A review," published in *ICETTA-2014*,ISBN-978-81-923777-7-3, 7 and 8th march 2014.
- 5] A. Raut, N. Bhagat, "Energy Efficient Transimpedance Amplifier," Published in *IEEE International Conference on Machine Intelligence Research Advancement (ICMIRA) Proceedings*, ISBN-978-0-7695-5013-8, pp. 345-348, Oct. 2013.

• National conferences : 02

- 1] “Interactive Voice Response System for Institute Automation”, Published in National Conference on Electronic, Communication and Information Security, April 2011.
- 2] “An overview of Interactive Voice Response Systems”, Published in National Conference on Electronic, Communication and Information Security, April 2011.