


Department of Electronics and Telecommunication Engineering				
Name	Mrs. Sneha Shailesh Revankar			
Date of Birth	24-11-1971			
Education Qualifications	UG	PG	PhD	
	BE	M.Tech(Digital Electronics)	--	
Work Experience	Teaching	Research	Industry	Others
	24	-	-	-
Area of Specialization	Digital Electronics			
Courses taught	1. Digital VLSI 2. Analog VLSI 3. Nano electronics 4. Mixed Signal VLSI 5. Basic Electrical 6. Basic Electronics 7. C Programming 8. Software Simulation Workshop			
Research guidance (Number of Students)	Under Graduate projects	16		
	Masters	4		
	Ph.D (Completed / Thesis Submitted / Ongoing)	-		
External Fund Received	1. Topic Name: FPGA Based Computer Network Application Development. Total Grant received by Mumbai University: 1,17,192/- for academic year 2010-11.			

	<p>2. Topic Name: FPGA Based Prototype of Blood Glucometer. Total Grant received by Mumbai University: 35,000/- for academic year 2013-14.</p> <p>3. Topic Name: Railway Based Safety and Security System Total Grant received by Mumbai University: 35,000/- for academic year 2017-18.</p>			
Patent (Filed / published / Granted) details	-			
No. of papers published in National/International Journals/Conferences	1	3	7	6
Research Publications (No. of papers in National/International Journals / Conferences and No. of Books/ Book Chapters published	Scopus/SCI indexed journals: - Peer reviewed / UGC journals: 2 IEEE/Springer conferences: - Books/Book Chapters: - Other International Conferences: 8 National Conference: 6			
Projects Carried out	16 BE Projects 04 ME Projects 03 Mumbai University Minor research grants			
Other major responsibilities	Department Level Exam Cell Incharge			