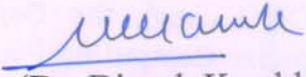**CIRCULAR:-**

Attention of the Principals of the affiliated Colleges and Directors of the recognized Institutions in Science & Technology Faculty is invited to this office Circular No. UG/239 of 2010, dated 12th August, 2010 relating to syllabus of the Bachelor of Engineering (B.E.) degree course.

They are hereby informed that the recommendations made by the Ad-hoc Board of Studies in Electronics & Telecommunication Engineering at its meeting held on 9th April, 2018 have been accepted by the Academic Council at its meeting held on 5th May, 2018 **vide** item No. 4.53 and that in accordance therewith, the revised syllabus as per the (CBCS) for the T.E. & B.E. in Electronics & Telecommunication Engineering (Sem - V to VIII) has been brought into force with effect from the academic year 2018-19 and 2019-2020, accordingly. (The same is available on the University's website www.mu.ac.in).

MUMBAI – 400 032

25th June, 2018

(Dr. Dinesh Kamble)

I/c REGISTRAR

To

The Principals of the affiliated Colleges & Directors of the recognized Institutions in Science & Technology Faculty. (Circular No. UG/334 of 2017-18 dated 9th January, 2018.)
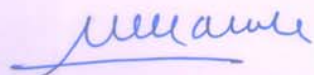
A.C/4.53/05/05/2018

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No. UG/42 -A of 2018   MUMBAI-400 032   25th June, 2018

Copy forwarded with Compliments for information to:-

1) The I/c Dean, Faculty of Science & Technology,
2) The Chairman, Ad-hoc Board of Studies in Electronics & Telecommunication Engineering,
3) The Director, Board of Examinations and Evaluation,
4) The Director, Board of Students Development,
5) The Co-Ordinator, University Computerization Centre,

(Dr. Dinesh Kamble)

I/c REGISTRAR

UNIVERSITY OF MUMBAI

Revised syllabus (Rev- 2016) from Academic Year 2016 -17
Under

FACULTY OF TECHNOLOGY

Electronics and Telecommunication Engineering

Third Year with Effect from **AY 2018-19**
Final Year with Effect from **AY 2019-20**

As per **Choice Based Credit and Grading System**
with effect from the AY 2016–17

Co-ordinator, Faculty of Technology's Preamble:

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) and give freedom to affiliated Institutes to add few (PEO's). It is also resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges and experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, and developed curriculum accordingly. In addition to outcome based education, semester based credit and grading system is also introduced to ensure quality of engineering education.

Choice based Credit and Grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes and Faculty of Technology has devised a transparent credit assignment policy and adopted ten points scale to grade learner's performance. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 12-13 weeks and remaining 2-3 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

Choice based Credit and grading system is implemented from the academic year 2016-17 through optional courses at department and institute level. This will be effective for SE, TE and BE from academic year 2017-18, 2018-19 and 2019-20 respectively.

Dr. S. K. Ukarande
Co-ordinator,
Faculty of Technology,
Member - Academic Council
University of Mumbai, Mumbai

Chairman's Preamble:

The curriculum in higher education is a living entity. It evolves with time; it reflects the ever changing needs of the society and keeps pace with the growing talent of the students and the faculty. The engineering education in India is expanding in manifolds and the main challenge is the quality of education. All stakeholders are very much concerned about it. The curriculum of Electronics & Telecommunication in Mumbai University is no exception. In keeping with the demands of the changing times, it contains innovative features. The exposure to the latest technology and tools used all over the world is given by properly selecting the subjects. It is designed in such a way to incorporate the requirements of various industries. The major emphasis of this process is to measure the outcomes of the program. Program outcomes are essentially a range of skills and knowledge that a student will have at the time of post-graduation. So the curriculum must be refined and updated to ensure that the defined objectives and outcomes are achieved.

I, as Chairman Ad-hoc Board of Studies in Electronics and Telecommunication Engineering, University of Mumbai, happy to state here that, the heads of the department and senior faculty from various institutes took timely and valuable initiative to frame the Program Educational objectives as listed below.

Objectives:

1. To produce Electronics & Telecommunication engineers, having strong theoretical foundation, good design experience and exposure to research and development.

2. To produce researcher who have clear thinking, articulation and interest to carry out theoretical and/or applied research resulting in significant advancement in the field of specialization.

3. To develop an ability to identify, formulate and solve electronics and telecommunication engineering problems in the latest technology.

4. To develop the ability among students to synthesize data and technical concepts from applications to product design.

These are the suggested and expected main objectives, individual affiliated institutes may add further in the list. I believe that the small step taken in the right direction will definitely help in providing quality education to the stake holders.

This book of curricula is the culmination of large number of faculty members and supporting staff. It also reflects the creative contribution of hundreds of teachers – both serving and retired. I sincerely hope that the faculty and students of Electronics and Telecommunication in Mumbai University will take full advantage of dynamic features of curriculum and make teaching-learning process a truly sublime experience for all.

At the end I must extend my gratitude to all experts and colleagues who contributed to make curriculum competent at par with latest technological development in the field of Electronics & Telecommunication Engineering.

Dr. Uttam D. Kolekar
Chairman, Ad-hoc Board of Studies in Electronics and Telecommunication Engineering

Program Structure for
B.E. Electronics & Telecommunication Engineering (Rev. 2016)
University of Mumbai (With Effect from 2017-2018)
Semester V

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC501	Microprocessor & Peripherals Interfacing	4	-	-	4	-	4
ECC502	Digital Communication	4	-	-	4	-	4
ECC503	Electromagnetic Engineering	4	-	1@	4	1	5
ECC504	Discrete Time Signal Processing	4	-	-	4	-	4
ECCDLO 501X	Department Level Optional Course I	4	-	-	4	-	4
ECL501	Microprocessor & Peripherals Interfacing Lab	-	2	-	-	1	1
ECL502	Digital Communication Lab	-	2	-	-	1	1
ECL503	Business Communication & Ethics Lab	-	2+2*	-	-	2	2
ECL504	Open Source Technology for Communication Lab	-	2	-	-	1	1
ECLDLO 501X	Department Level Optional Lab I	-	-	2#	-	1	1
Total		**20**	**10**	**3**	**20**	**7**	**27**

@ 1 hour to be taken as tutorial classwise   #2 hours to be taken as either lab or tutorial based on subject requirement
*2 hours to be taken as tutorial batchwise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral/ Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC501	Microprocessor & Peripherals Interfacing	20	20	20	80	03	--	--	100
ECC502	Digital Communication	20	20	20	80	03	--	--	100
ECC503	Electromagnetic Engineering	20	20	20	80	03	25	--	125
ECC504	Discrete Time Signal Processing	20	20	20	80	03	--	--	100
ECCDLO 501X	Department Level Optional Course I	20	20	20	80	03	--	--	100
ECL501	Microprocessor & Peripherals Interfacing Lab	--	--	--	--	--	25	25	50
ECL502	Digital Communication Lab	--	--	--	--	--	25	25	50
ECL503	Business Communication & Ethics Lab	--	--	--	--	--	50	--	50
ECL504	Open Source Technology for Communication Lab	--	--	--	--	--	25	25	50
ECLDLO 501X	Department Level Optional Lab I	--	--	--	--	--	25	--	25
Total				**100**	**400**		**175**	**75**	**750**

Course Code	Department Level Optional Course I
ECCDLO 5011	Microelectronics
ECCDLO 5012	TV & Video Engineering
ECCDLO 5013	Finite Automata Theory
ECCDLO 5014	Data Compression and Encryption

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC601	Microcontrollers & Applications	4	-	--	4	--	4
ECC602	Computer Communication Networks	4	-	-	4	-	4
ECC603	Antenna & Radio Wave Propagation	4	-	-	4	-	4
ECC604	Image Processing and Machine Vision	4	-	--	4	--	4
ECCDLO 602X	Department Level Optional Course II	4	-	-	4	-	4
ECL601	Microcontroller & Applications Lab	-	2	-	-	1	1
ECL602	Computer Communication Network Lab	-	2	-	-	1	1
ECL603	Antenna & Radio Wave Propagation Lab	-	2	-	-	1	1
ECL604	Image Processing and Machine Vision Lab	-	2	-	-	1	1
ECLDLO 602X	Department Level Optional Lab II	-	2	-	-	1	1
Total		**20**	**10**	**-**	**20**	**5**	**25**

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC601	Microcontroller& Applications	20	20	20	80	03	--	--	100
ECC602	Computer Communication Network	20	20	20	80	03	--	--	100
ECC603	Antenna & Radio Wave Propagation	20	20	20	80	03	--	--	100
ECC604	Image Processing and Machine Vision Lab	20	20	20	80	03	--	--	100
ECCDLO 602X	Department Level Optional Course II	20	20	20	80	03	--	--	100
ECL601	Microcontroller & Applications Lab	--	--	--	--	--	25	25	50
ECL602	Computer Communication Network Lab	--	--	--	--	--	25	25	50
ECL603	Antenna & Radio Wave Propagation Lab	--	--	--	--	--	25	25	50
ECL604	Image Processing and Machine Vision Lab	--	--	--	--	--	25	25	50
ECLDLO 602X	Department Level Optional Lab II	--	--	--	--	--	25	--	25
Total				**100**	**400**		**125**	**100**	**725**

Course Code	Department Level Optional Course II
ECCDLO 6021	Digital VLSI Design
ECCDLO 6022	Radar Engineering
ECCDLO 6023	Database Management System
ECCDLO 6024	Audio Processing