



**Fr. C. Rodrigues Institute of Technology, Vashi**

**Department of Electronics & Telecommunication Engineering**

**Time-Table (SH 2021)**

**CLASS: TE EXTC SEM V**

	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
09.00-10.00	DTSP	DCOM	RSA	PC & E	DSA	DTSP (R) ~
10.15-11.15	RSA	DSA	DTSP	DSA	DTSP	RSA (R) ~
11.30-12.30	DVLSI	PC & E	DVLSI	DCOM	RSA	DCOM(R) ~
12.30-1.30	LUNCH BREAK					
1.30-2.30	DCOM	DVLSI	PC & E Lab-B1 & B2	DVLSIL-B1 DTSPL-B2	DCOM LAB	DSA(R) ~
2.30-3.30	*DTSPL-B1 DVLSIL-B2	*MP 2A-B1 MP 2A-B2				DVLSI(R) ~
3.30-4.30	*Timing: 2.45-4.45	*Timing: 2.45-4.45	RSA (Tuts)	PLACEMENT		
Signatures	Time Table Co-ordinator			HOD		

**THEORY:**

RSA- Dr. Milind Shah  
 DCOM-Ms. Sadhana P.  
 DTSP-Dr. Savitha U  
 DSA-Mr. Yogesh C.  
 DVLSI-Ms. Sneha R.  
 Professional Commn. And Ethics- Ms. Vaishali

**1. (R) ~ indicates Remedial lectures**

**PRACTICALS:**

DCOM LAB- Mrs. Sadhana P.  
 DTSPL- Dr. Savita U.  
 MP 2A- Mr. Yogesh C., Ms. Manita R.  
 DVLSIL- Dr. Anant Kulkarni.  
 PC & E Lab- Ms. Vaishali .

**CLASS TEACHERS: 1. Ms. Sadhana P.**

**2. Dr. Savitha U.**