

AC 14/7/2016, Item No. 4.64

# UNIVERSITY OF MUMBAI



## Bachelor of Engineering

First Year Engineering (Semester I & II), Revised course  
(REV- 2016) from Academic Year 2016 – 17,  
(Common for All Branches of Engineering)

(As per **Choice Based Credit and Grading System**  
with effect from the academic year 2016–2017)

## **From Coordinator's Desk:-**

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) give freedom to affiliated Institutes to add few (PEO's) course objectives course outcomes to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, developed curriculum accordingly. In addition to outcome based education, **Choice Based Credit and Grading System** is also introduced to ensure quality of engineering education.

Choice Based Credit and Grading System enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes Faculty of Technology has devised a transparent credit assignment policy adopted ten points scale to grade learner's performance. Credit grading based system was implemented for First Year of Engineering from the academic year 2016-2017. Subsequently this system will be carried forward for Second Year Engineering in the academic year 2017-2018, for Third Year Final Year Engineering in the academic years 2018-2019, 2019-2020, respectively.

**Dr. S. K. Ukarande**  
**Co-ordinator,**  
**Faculty of Technology,**  
**Member - Academic Council**  
**University of Mumbai, Mumbai**

**Program Structure for  
First Year Engineering (Semester I & II)  
Mumbai University  
(With Effect from 2016-2017)**

**Semester I**

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned					
		Theory	Pract.	Tut.	Theory	TW/Pract	Tut.	Total		
FEC101	Applied Mathematics-I	04	-	01	04	-	01	05		
FEC102	Applied Physics-I	03	01	-	03	0.5	-	3.5		
FEC103	Applied Chemistry -I	03	01	-	03	0.5	-	3.5		
FEC104	Engineering Mechanics	05	02	-	05	01	-	06		
FEC105	Basic Electrical Engineering	04	02	-	04	01	-	05		
FEC106	Environmental studies	02	-	-	02	-	-	02		
FEL101	Basic Workshop Practice-I	-	04	-	-	02	-	02		
<b>Total</b>		<b>21</b>	<b>10</b>	<b>01</b>	<b>21</b>	<b>05</b>	<b>01</b>	<b>27</b>		
Course Code	Course Name	Examination Scheme								
		Theory				End Sem Exam	Term Work	Pract	Oral	Total
		Internal Assessment								
		Test1	Test2	Avg						
FEC101	Applied Mathematics-I	20	20	20	80	25	-	-	125	
FEC102	Applied Physics-I	15	15	15	60	25	-	-	100	
FEC103	Applied Chemistry -I	15	15	15	60	25	-	-	100	
FEC104	Engineering Mechanics	20	20	20	80	25	-	25	150	
FEC105	Basic Electrical Engineering	20	20	20	80	25	-	25	150	
FEC106	Environmental studies	15	15	15	60	-	-	-	75	
FEL101	Basic Workshop Practice-I	-	-	-	-	50	-	-	50	
<b>Total</b>				<b>105</b>	<b>420</b>	<b>175</b>		<b>50</b>	<b>750</b>	

## Semester II

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned				
		Theory	Pract.	Tut.	Theory	TW/Pract	Tut.	Total	
FEC201	Applied Mathematics-II	04	-	01	04	-	01	<b>05</b>	
FEC202	Applied Physics-II	03	01	-	03	0.5	-	<b>3.5</b>	
FEC203	Applied Chemistry -II	03	01	-	03	0.5	-	<b>3.5</b>	
FEC204	Engineering Drawing	03	04	-	03	02	-	<b>05</b>	
FEC205	Structured Programming Approach	04	02	-	04	01	-	<b>05</b>	
FEC206	Communication Skills	02	02	-	02	01	-	<b>03</b>	
FEL201	Basic Workshop Practice-II	-	04	-	-	02	-	<b>02</b>	
<b>Total</b>		<b>19</b>	<b>14</b>	<b>01</b>	<b>19</b>	<b>07</b>	<b>01</b>	<b>27</b>	
Course Code	Course Name	Examination Scheme							
		Theory				Term Work	Pract	Oral	Total
		Internal Assessment			End Sem Exam				
		Test1	Test2	Avg					
FEC201	Applied Mathematics-II	20	20	20	80	25	-	-	<b>125</b>
FEC202	Applied Physics-II	15	15	15	60	25	-	-	<b>100</b>
FEC203	Applied Chemistry -II	15	15	15	60	25	-	-	<b>100</b>
FEC204	Engineering Drawing	15	15	15	60	25	50	-	<b>150</b>
FEC205	Structured Programming Approach	20	20	20	80	25	25	-	<b>150</b>
FEC206	Communication Skills	10	10	10	40	25	-	-	<b>75</b>
FEL201	Basic Workshop Practice-II	-	-	-	-	50	-	-	<b>50</b>
<b>Total</b>				<b>95</b>	<b>380</b>	<b>200</b>	<b>75</b>	<b>-</b>	<b>750</b>

AC-11.05.2017

Item No. 4.210

# UNIVERSITY OF MUMBAI



Revised syllabus (Rev- 2016) from Academic Year 2016 -17  
Under

## FACULTY OF TECHNOLOGY

### **Electronics and Telecommunication Engineering**

**Second Year** with Effect from AY 2017-18

**Third Year** with Effect from AY 2018-19

**Final Year** with Effect from AY 2019-20

As per **Choice Based Credit and Grading System**

with effect from the AY 2016-17

### **Co-ordinator, Faculty of Technology's Preamble:**

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) and give freedom to affiliated Institutes to add few (PEO's). It is also resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges and experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, and developed curriculum accordingly. In addition to outcome based education, semester based credit and grading system is also introduced to ensure quality of engineering education.

Choice based Credit and Grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes and Faculty of Technology has devised a transparent credit assignment policy and adopted ten points scale to grade learner's performance. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 12-13 weeks and remaining 2-3 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

Choice based Credit and grading system is implemented from the academic year 2016-17 through optional courses at department and institute level. This will be effective for SE, TE and BE from academic year 2017-18, 2018-19 and 2019-20 respectively.

**Dr. S. K. Ukarande**  
**Co-ordinator,**  
**Faculty of Technology,**  
**Member - Academic Council**  
**University of Mumbai, Mumbai**

**Chairman's Preamble:**

The curriculum in higher education is a living entity. It evolves with time; it reflects the ever changing needs of the society and keeps pace with the growing talent of the students and the faculty. The engineering education in India is expanding in manifolds and the main challenge is the quality of education. All stakeholders are very much concerned about it. The curriculum of Electronics & Telecommunication in Mumbai University is no exception. In keeping with the demands of the changing times, it contains innovative features. The exposure to the latest technology and tools used all over the world is given by properly selecting the subjects. It is designed in such a way to incorporate the requirements of various industries. The major emphasis of this process is to measure the outcomes of the program. Program outcomes are essentially a range of skills and knowledge that a student will have at the time of post-graduation. So the curriculum must be refined and updated to ensure that the defined objectives and outcomes are achieved.

I, as Chairman Ad-hoc Board of Studies in Electronics and Telecommunication Engineering, University of Mumbai, happy to state here that, the heads of the department and senior faculty from various institutes took timely and valuable initiative to frame the Program Educational objectives as listed below.

**Objectives:**

1. To produce Electronics & Telecommunication engineers, having strong theoretical foundation, good design experience and exposure to research and development.
2. To produce researcher who have clear thinking, articulation and interest to carry out theoretical and/or applied research resulting in significant advancement in the field of specialization.
3. To develop an ability to identify, formulate and solve electronics and telecommunication engineering problems in the latest technology.
4. To develop the ability among students to synthesize data and technical concepts from applications to product design.

These are the suggested and expected main objectives, individual affiliated institutes may add further in the list. I believe that the small step taken in the right direction will definitely help in providing quality education to the stake holders.

This book of curricula is the culmination of large number of faculty members and supporting staff. It also reflects the creative contribution of hundreds of teachers – both serving and retired. I sincerely hope that the faculty and students of Electronics and Telecommunication in Mumbai University will take full advantage of dynamic features of curriculum and make teaching-learning process a truly sublime experience for all.

At the end I must extend my gratitude to all experts and colleagues who contributed to make curriculum competent at par with latest technological development in the field of Electronics & Telecommunication Engineering.

**Dr. Uttam D. Kolekar**

**Chairman, Ad-hoc Board of Studies in Electronics and Telecommunication Engineering**

**Program Structure for  
B.E. Electronics & Telecommunication Engineering (Rev. 2016)  
University of Mumbai  
(With Effect from 2017-2018)**

**Semester III**

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC301	Applied Mathematics- III	4	-	2@	4	1	5
ECC302	Electronic Devices and Circuits I	4	-	-	4	-	4
ECC303	Digital System Design	4	-	-	4	-	4
ECC304	Circuit Theory and Networks	4	-	2@	4	1	5
ECC305	Electronic Instrumentation and Control	4	-	2@	4	1	5
ECL301	Electronic Devices and Circuits I Laboratory	-	2	-	-	1	1
ECL302	Digital System Design Laboratory	-	2	-	-	1	1
ECL303	OOP using JAVA Laboratory	-	2	-	-	1	1
<b>Total</b>		<b>20</b>	<b>6</b>	<b>6</b>	<b>20</b>	<b>6</b>	<b>26</b>

@ 2 hour to be taken as tutorial classwise

Course Code	Course Name	Examination Scheme							Total
		Theory			End Sem Exam	Exam Duration (Hrs)	TW	Oral/ Prac	
		Internal Assessment							
Test1	Test 2	Avg							
ECC301	Applied Mathematics-III	20	20	20	80	03	25	--	125
ECC302	Electronic Devices and Circuits I	20	20	20	80	03	--	--	100
ECC303	Digital System Design	20	20	20	80	03	--	--	100
ECC304	Circuit Theory and Networks	20	20	20	80	03	25	--	125
ECC305	Electronic Instrumentation and Control	20	20	20	80	03	25	--	125
ECL301	Electronic Devices and Circuits I Laboratory	--	--	--	--	--	25	25	50
ECL302	Digital System Design Laboratory	--	--	--	--	--	25	25	50
ECL303	OOP using JAVA Laboratory	--	--	--	--	--	25	25	50
<b>Total</b>				<b>100</b>	<b>400</b>		<b>150</b>	<b>75</b>	<b>725</b>



**Semester IV**

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC401	Applied Mathematics- IV	4	-	2@	4	1	5
ECC402	Electronic Devices and Circuits II	4	-	-	4	-	4
ECC403	Linear Integrated Circuits	4	-	-	4	-	4
ECC404	Signals & Systems	4	-	2@	4	1	5
ECC405	Principles of Communication Engineering	4	-	-	4	-	4
ECL401	Electronic Devices and Circuits II Laboratory	-	2	-	-	1	1
ECL402	Linear Integrated Circuits Laboratory	-	2	-	-	1	1
ECL403	Principles of Communication Engineering Laboratory	-	2	-	-	1	1
<b>Total</b>		<b>20</b>	<b>6</b>	<b>4</b>	<b>20</b>	<b>5</b>	<b>25</b>

@ 2 hour to be taken as tutorial classwise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC401	Applied Mathematics- IV	20	20	20	80	03	25	--	125
ECC402	Electronic Devices and Circuits II	20	20	20	80	03	--	--	100
ECC403	Linear Integrated Circuits	20	20	20	80	03	--	--	100
ECC404	Signals & Systems	20	20	20	80	03	25	--	125
ECC405	Principles of Communication Engineering	20	20	20	80	03	--	--	100
ECL401	Electronic Devices and Circuits II Laboratory	--	--	--	--	--	25	25	50
ECL402	Linear Integrated Circuits Laboratory	--	--	--	--	--	25	25	50
ECL403	Principles of Communication Engineering Laboratory	--	--	--	--	--	25	25	50
<b>Total</b>				<b>100</b>	<b>400</b>		<b>125</b>	<b>75</b>	<b>700</b>

AC- 5.05.2018

Item No. 4.53

# UNIVERSITY OF MUMBAI



Revised syllabus (Rev- 2016) from Academic Year 2016 -17  
Under

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**Third Year** with Effect from AY 2018-19

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As per **Choice Based Credit and Grading System**

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**Program Structure for  
B.E. Electronics & Telecommunication Engineering (Rev. 2016)  
University of Mumbai (With Effect from 2017-2018)  
Semester V**

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC501	Microprocessor & Peripherals Interfacing	4	-	-	4	-	4
ECC502	Digital Communication	4	-	-	4	-	4
ECC503	Electromagnetic Engineering	4	-	1@	4	1	5
ECC504	Discrete Time Signal Processing	4	-	-	4	-	4
<b>ECCDLO 501X</b>	<b>Department Level Optional Course I</b>	4	-	-	4	-	4
ECL501	Microprocessor & Peripherals Interfacing Lab	-	2	-	-	1	1
ECL502	Digital Communication Lab	-	2	-	-	1	1
ECL503	Business Communication & Ethics Lab	-	2+2*	-	-	2	2
ECL504	Open Source Technology for Communication Lab	-	2	-	-	1	1
<b>ECLDLO 501X</b>	<b>Department Level Optional Lab I</b>	-	-	2#	-	1	1
<b>Total</b>		<b>20</b>	<b>10</b>	<b>3</b>	<b>20</b>	<b>7</b>	<b>27</b>

@ 1 hour to be taken as tutorial classwise #2 hours to be taken as either lab or tutorial based on subject requirement  
\*2 hours to be taken as tutorial batchwise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral/ Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC501	Microprocessor & Peripherals Interfacing	20	20	20	80	03	--	--	100
ECC502	Digital Communication	20	20	20	80	03	--	--	100
ECC503	Electromagnetic Engineering	20	20	20	80	03	25	--	125
ECC504	Discrete Time Signal Processing	20	20	20	80	03	--	--	100
<b>ECCDLO 501X</b>	<b>Department Level Optional Course I</b>	20	20	20	80	03	--	--	100
ECL501	Microprocessor & Peripherals Interfacing Lab	--	--	--	--	--	25	25	50
ECL502	Digital Communication Lab	--	--	--	--	--	25	25	50
ECL503	Business Communication & Ethics Lab	--	--	--	--	--	50	--	50
ECL504	Open Source Technology for Communication Lab	--	--	--	--	--	25	25	50
<b>ECLDLO 501X</b>	<b>Department Level Optional Lab I</b>	--	--	--	--	--	25	--	25
<b>Total</b>				<b>100</b>	<b>400</b>		<b>175</b>	<b>75</b>	<b>750</b>

<b>Course Code</b>	<b>Department Level Optional Course I</b>
ECCDLO 5011	Microelectronics
ECCDLO 5012	TV & Video Engineering
ECCDLO 5013	Finite Automata Theory
ECCDLO 5014	Data Compression and Encryption

**Semester VI**

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC601	Microcontrollers & Applications	4	-	--	4	--	4
ECC602	Computer Communication Networks	4	-	-	4	-	4
ECC603	Antenna & Radio Wave Propagation	4	-	-	4	-	4
ECC604	Image Processing and Machine Vision	4	-	--	4	--	4
<b>ECCDLO 602X</b>	<b>Department Level Optional Course II</b>	4	-	-	4	-	4
ECL601	Microcontroller & Applications Lab	-	2	-	-	1	1
ECL602	Computer Communication Network Lab	-	2	-	-	1	1
ECL603	Antenna & Radio Wave Propagation Lab	-	2	-	-	1	1
ECL604	Image Processing and Machine Vision Lab	-	2	-	-	1	1
<b>ECLDLO 602X</b>	<b>Department Level Optional Lab II</b>	-	2	-	-	1	1
<b>Total</b>		<b>20</b>	<b>10</b>	<b>-</b>	<b>20</b>	<b>5</b>	<b>25</b>

Course Code	Course Name	Examination Scheme									
		Theory					End Sem Exam	Exam Duration (Hrs)	TW	Oral & Prac	Total
		Internal Assessment			Avg						
		Test1	Test 2	Avg							
ECC601	Microcontroller & Applications	20	20	20	80	03	--	--	100		
ECC602	Computer Communication Network	20	20	20	80	03	--	--	100		
ECC603	Antenna & Radio Wave Propagation	20	20	20	80	03	--	--	100		
ECC604	Image Processing and Machine Vision Lab	20	20	20	80	03	--	--	100		
<b>ECCDLO 602X</b>	<b>Department Level Optional Course II</b>	20	20	20	80	03	--	--	100		
ECL601	Microcontroller & Applications Lab	--	--	--	--	--	25	25	50		
ECL602	Computer Communication Network Lab	--	--	--	--	--	25	25	50		
ECL603	Antenna & Radio Wave Propagation Lab	--	--	--	--	--	25	25	50		
ECL604	Image Processing and Machine Vision Lab	--	--	--	--	--	25	25	50		
<b>ECLDLO 602X</b>	<b>Department Level Optional Lab II</b>	--	--	--	--	--	25	--	25		
<b>Total</b>				<b>100</b>	<b>400</b>		<b>125</b>	<b>100</b>	<b>725</b>		

<b>Course Code</b>	<b>Department Level Optional Course II</b>
ECCDLO 6021	Digital VLSI Design
ECCDLO 6022	Radar Engineering
ECCDLO 6023	Database Management System
ECCDLO 6024	Audio Processing



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**University of Mumbai, Mumbai**

**Chairman's Preamble:**

The curriculum in higher education is a living entity. It evolves with time; it reflects the ever changing needs of the society and keeps pace with the growing talent of the students and the faculty. The engineering education in India is expanding in manifolds and the main challenge is the quality of education. All stakeholders are very much concerned about it. The curriculum of Electronics & Telecommunication in Mumbai University is no exception. In keeping with the demands of the changing times, it contains innovative features. The exposure to the latest technology and tools used all over the world is given by properly selecting the subjects. It is designed in such a way to incorporate the requirements of various industries. The major emphasis of this process is to measure the outcomes of the program. Program outcomes are essentially a range of skills and knowledge that a student will have at the time of post-graduation. So the curriculum must be refined and updated to ensure that the defined objectives and outcomes are achieved.

I, as Chairman Ad-hoc Board of Studies in Electronics and Telecommunication Engineering, University of Mumbai, happy to state here that, the heads of the department and senior faculty from various institutes took timely and valuable initiative to frame the Program Educational objectives as listed below.

**Objectives:**

1. To produce Electronics & Telecommunication engineers, having strong theoretical foundation, good design experience and exposure to research and development.
2. To produce researcher who have clear thinking, articulation and interest to carry out theoretical and/or applied research resulting in significant advancement in the field of specialization.
3. To develop an ability to identify, formulate and solve electronics and telecommunication engineering problems in the latest technology.
4. To develop the ability among students to synthesize data and technical concepts from applications to product design.

These are the suggested and expected main objectives, individual affiliated institutes may add further in the list. I believe that the small step taken in the right direction will definitely help in providing quality education to the stake holders.

This book of curricula is the culmination of large number of faculty members and supporting staff. It also reflects the creative contribution of hundreds of teachers – both serving and retired. I sincerely hope that the faculty and students of Electronics and Telecommunication in Mumbai University will take full advantage of dynamic features of curriculum and make teaching-learning process a truly sublime experience for all.

At the end I must extend my gratitude to all experts and colleagues who contributed to make curriculum competent at par with latest technological development in the field of Electronics & Telecommunication Engineering.

**Dr. Uttam D. Kolekar****Chairman, Ad-hoc Board of Studies in Electronics and Telecommunication Engineering**

**Semester VII**

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC701	Microwave Engineering	4	-	-	4	-	4
ECC702	Mobile Communication System	4	-	-	4	-	4
ECC703	Optical Communication	4	-	--	4	-	4
<b>ECCDLO 703X</b>	<b>Department Level Optional Course III</b>	4	-	-	4	-	4
<b>ILO701X</b>	<b>Institute Level Optional Course I</b>	3	-	-	3	-	3
ECL701	Microwave Engineering Lab	-	2	-	-	1	1
ECL702	Mobile Communication System Lab	-	2	-	-	1	1
ECL703	Optical Communication Lab	-	2	-	-	1	1
<b>ECLDLO 703X</b>	<b>Department Level Optional Lab III</b>	-	2	-	-	1	1
ECL704	Project-I	-	6	-	-	3	3
<b>Total</b>		<b>19</b>	<b>14</b>	<b>-</b>	<b>19</b>	<b>7</b>	<b>26</b>

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC701	Microwave Engineering	20	20	20	80	03	--	--	100
ECC702	Mobile Communication System	20	20	20	80	03	--	--	100
ECC703	Optical Communication	20	20	20	80	03	--	--	100
<b>ECCDLO 703X</b>	<b>Department Level Optional Course III</b>	20	20	20	80	03	--	--	100
<b>ILO701X</b>	<b>Institute Level Optional Course I</b>	20	20	20	80	03	--	--	100
ECL701	Microwave Engineering Lab	--	--	--	--	--	25	25	50
ECL702	Mobile Communication System Lab	--	--	--	--	--	25	25	50
ECL703	Optical Communication Lab	--	--	--	--	--	25	25	50
<b>ECLDLO 703X</b>	<b>Department Level Optional Lab III</b>	--	--	--	--	--	25	25	50
ECL704	Project-I	--	--	--	--	--	50	50	100
<b>Total</b>				<b>100</b>	<b>400</b>		<b>150</b>	<b>150</b>	<b>800</b>

<b>Course Code</b>	<b>Department Level Optional Course III</b>	<b>Course Code</b>	<b>Institute Level Optional Course I<sup>#</sup></b>
ECCDLO7031	Neural Networks and Fuzzy Logic	ILO7011	Product Lifecycle Management
ECCDLO7032	Big Data Analytics	ILO7012	Reliability Engineering
ECCDLO7033	Internet Communication Engineering	ILO7013	Management Information System
ECCDLO7034	CMOS Mixed Signal VLSI	ILO7014	Design of Experiments
ECCDLO7035	Embedded System	ILO7015	Operation Research
		ILO7016	Cyber Security and Laws
		ILO7017	Disaster Management and Mitigation Measures
		ILO7018	Energy Audit and Management
		ILO7019	Development Engineering

**# Common with all branches**

### Semester VIII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC801	RF Design	4	-	--	4	--	4
ECC802	Wireless Networks	4	-	-	4	-	4
ECCDLO 804X	Department Level Optional Course IV	4	-	-	4	-	4
ILO802X	Institute Level Optional Course II	3	-	-	3	-	3
ECL801	RF Design Lab	-	2	-	-	1	1
ECL802	Wireless Networks Lab	-	2	-	-	1	1
ECLDLO 804X	Department Level Optional Lab IV	-	2	-	-	1	1
ECL803	Project-II	-	12	-	-	6	6
<b>Total</b>		<b>15</b>	<b>18</b>	<b>-</b>	<b>15</b>	<b>9</b>	<b>24</b>

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC801	RF Design	20	20	20	80	03	--	--	100
ECC802	Wireless Networks	20	20	20	80	03	--	--	100
ECCDLO 804X	Department Level Optional Course IV	20	20	20	80	03	--	--	100
ILO802X	Institute Level Optional Course II	20	20	20	80	03	--	--	100
ECL801	RF Design Lab	--	--	--	--	--	25	25	50
ECL802	Wireless Networks Lab	--	--	--	--	--	25	25	50
ECLDLO 804X	Department Level Optional Lab IV	--	--	--	--	--	25	25	50
ECL803	Project-II	--	--	--	--	--	100	50	150
<b>Total</b>				<b>80</b>	<b>320</b>		<b>175</b>	<b>125</b>	<b>700</b>

<b>Course Code</b>	<b>Department Level Elective Course IV</b>	<b>Course Code</b>	<b>Institute Level Elective Course II<sup>#</sup></b>
ECCDLO8041	Optical Networks	ILO8021	Project Management
ECCDLO8042	Advanced Digital Signal Processing	ILO8022	Finance Management
ECCDLO8043	Satellite Communication	ILO8023	Entrepreneurship Development and Management
ECCDLO8044	Network management in Telecommunication	ILO8024	Human Resource Management
		ILO8025	Professional Ethics and CSR
		ILO8026	Research Methodology
		ILO8027	IPR and Patenting
		ILO8028	Digital Business Management
		ILO8029	Environmental Management

**# Common with all branches**